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# Utility Patent Application Transmittal

(only for nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No. TAI 129

DATE: July 25, 2001

Inventor (s): Takahiro OKA and Makoto TERUI

TITLE: SEMICONDUCTOR CHIP PACKAGE

**APPLICATION ELEMENTS**  
See MPEP chapter 600 concerning utility patent application contents.

**Commissioner for Patents**  
**ADDRESS TO: Box Patent Application**  
**Washington, DC 20231**

1. ☒ Patent Application Fee Determination Record  
(Submit an original, and a duplicate for fee processing)

2. ☒ Specification [Total Pages 24]  
(preferred arrangement set forth below)  
- Descriptive title of the Invention  
- Cross References to Related Applications  
- Background of the Invention  
- Brief Summary of the Invention  
- Brief Description of the Drawings (if filed)  
- Detailed Description  
- Claim(s)  
- Abstract of the Disclosure

3. ☒ Drawing(s) Figures 1-13 [Total Sheets 9]

4. Oath or Declaration [Total Sheets 3]  
☒ Newly executed (original or copy)

## ACCOMPANYING APPLICATION PARTS

8. ☒ Assignment & Recordation Cover Sheet [Total pages 4]

9. ☐ Information Disclosure Statement (IDS)/PTO-1449  
[Total Pages ] [Total References ]

12. ☐ Preliminary Amendment [Total Pages ]

13. ☒ Return Receipt Postcard (MPEP 503)

14. ☐ Small Entity Assertion

15. ☐ Certified Copy of Priority Document(s)  
The rights of priority are claimed under 35 U.S.C. § 119 of  
Japanese Application No. 2001-26898 Filed February 2, 2001

16. ☐ Other: \_\_\_\_\_

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